U.S. DEPARTMENT OF COMMERCE SERIAL NO. Form PTO-1449 ATTY. DOCKET NO. PATENT AND TRADEMARK OFFICE 94100422(EP)USC1X1C1D11 09/779,382 (Modified) **PDDD** INFORMATION DISCLOSURE **EXAMINER:** APPLICANT: STATEMENT BY APPLICANT **Dustin NGUYEN** ADRIAN P. WISE et al. (Use several sheets if necessary) FILING DATE: Art Unit: 2154 Confirmation: 7169 February 8, 2001 \$37 CFR 1.98(b)) U.S. PATENT DOCUMENTS FILING DATE ISSUE **PATENT NUMBER** DATE PATENTEE CLASS **SUBCLASS** APPROPRIATE FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION

EXAMINER INITIAL	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION YES NO
pN	0196911	08/10/86	EP			
DN	0255767	10/02/88	EP		·	
DH	0468480	29/01/92	EP			
N	0572262	01/12/93	EP			
10N	0572263	01/12/93	EP			
DN	0576749	05/01/94	EP	† 		
	0589.734	30/03/94	EP			
DN	0618728	05/10/94	EP		·	
Na	0639032	15/02/95	EP			
DN	2 045 035	22/10/80	GB			
Na	2 059 724	23/04/81	GB			.
DN	2 171 578	28/08/86	GB			
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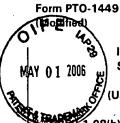
EXAMINER INITIAL	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION YES NO
<u>0</u> 5	9425935	11/10/94	wo			NO

OTHER DOCUMENTS (Including Author, Title, Date**, Relevant Pages, Place of Publication***)

EXAMINER INITIAL	DOCUMENT IDENTIFICATION
ON	CHONG, M. YONG: "A DATA FLOW ARCHITECTURE FOR DIGITAL IMAGE PROCESSING," WESCON TECH., PAPERS NO. 4/6, 10/30/84, ANAHEIM, CALIFORNIA, USA, PP. 1-10
DN	A. GUPTA ET AL.: "A FAST RECURSIVE ALGORITHM FOR COMPUTING THE DISCRETE SINE TRANSFORM," IEEE TRANSACTIONS ON ACOUSTICS, SPEECH, AND SIGNAL PROCESSING, VO. 38, NO. 3, MARCH 1990, IEEE PRESS, NEW YORK, US, PAGSE 553-557.
DN	ELLIOTT J. A. ET AL: "REAL-TIME SIMULATION OF VIDEOPHONE IMAGE CODING ALGORITHMS ON RECONFIGURABLE MULTICOMPUTERS," IEEE PROCEEDINGS E. COMPUTERS & DIGITAL TECHNIQUES, VOL. 139, NO. 3 PART E., 05/01/92, PP 269-279, XP0000306411.
DN	HONG, YANG-CHANG; T.H. PAYNE: "A HYBRID APPROACH FOR EFFICIENT DATAFLOW COMPUTING," COMPUTERS AND COMMUNICATIONS, 1990 INT'L PHOENIX CONFERENCE. IEEE PUBLICATIONS, MAY 1990, PP 170-178.
-DN	HSIEH S. HOU, " A FAST RECURSIVE ALGORITHM FOR COMPUTING THE DISCRETE COSINE TRANSFORM," IEEE TRANSACTIONS ON ACOUSTICS, SPEECH, AND SIGNAL PROCESSING, VO. 35, NO. 10, OCTOBER 1987, IEEE PRESS, NEW YORK, US, PAGES 1455-1461.
DN	KAORU, UCHIDA ET AL: "A PIPELINED DATAFLOW PROCESSOR ARCHITECTURE BASED ON A VARIABLE LENGTH TOKEN CONCEPT," ARCHITECTURE, UNIVERSITY PARK, AUG. 15 - 19, 1988, VOL. 1, 15 AUGUST 1988, BRIGGS F A, PP 209-216, XP000079309.
pr	KOMORI, SHINJI ET AL.: " AN ELASTIC PIPELINE MECHANISM BY SELF-TIMED CIRCUITS, IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. 23, NO. 1, 2/88, NEW YORK, NY, USA, PP. 111-117.
DN	KOPET, TOM: "PROGRAMMABLE ARCHITECTURES FOR REAL-TIME VIDEO COMPRESSION," 4TH INTERNATIONAL CONFERENCE ON SIGNAL PROCESSING APPLICATIONS & TECHNOLOGY, VOL. 2, 09/28/93 - 10/01/93, SANTA CLARA, CALIFORNIA, USA, PP 1031-1038.
00/	MAYER, A.C.: "THE ARCHITECTURE OF A SINGLE-CHIP PROCESSOR ARRAY FOR VIDEOCOMPRESSION," PROCEEDINGS OF THE INTERNATIONAL CONFERENCE ON CONSUMER ELECTRONICS, ROSEMONT, JUNE 8-10, 1993, NO. CONF. 12, 08/06/93, INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS, PP 294-95, XP0000427624.
ON	NORMILE, JAMES; DAN WRIGHT: "IMAGE COMPRESSION USING COARSE GRAIN PARALLEL PROCESSING", ICASSP 1991: ACOUSTICS, SPEECH & SIGNAL PROCESSING CONFERENCE, IEEE PUBLICATIONS. JULY 1991 PP 1121-1124
DN	TOKUMICHI MURAKAMI ET AL: "A DSP ARCHITECTURAL DESIGN FOR LOW BIT-RATE MOTION VIDEO CODEC," IEEE TRANSACTIONS ON CIRC UITS AND SYSTEMS, VOL. 36, NO. 10, 10/1/89, PP 1267-1274, XP000085313.
bn	H.R. WU, ET AL.: " A TWO DIMENSIONAL FAST COSINE TRANSFORM ALGORITHM BASED ON HOU'S
EXAMINER	DATE CONSIDERED 67/18/06

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EXAMINER INITIAL	DOCUMENT IDENTIFICATION		
	APPROACH." IEEE TRANSACTION ON ACQUISTICS, SPEECH & SIGNAL PROCESSING, VOL. 39, NO. 2, FEBRUARY 1991, IEEE PRESS, NEW YORK, US, PAGES 544-546.		
02	YANG, KUN-MIN: "VLSI ARCHITECTURE DESIGN OF A VERSATILE VARIABLE LENGTH DECODING CHIP FOR REAL TIME VIDEO CODECS," IEEE REGION 10 CONFERENCE ON COMPUTER AND COMMUNICATION SYSTEMS, IEEE PUBLICATIONS FEBRUARY 1990, PP 551-554.		
DN	P. YIP, ET AL.: "DIT AND DIF ALGORITHM FOR DISCRETE SINE AND COSINE TRANSFORMS" PROCEEDINGS OF THE INTERNATIONAL SYMPOSIUM ON CIRCUITS AND SYSTEMS, IEEE PRESS, NEW YORK, US, VOL. 2/3, 5 JUNE 1985, KYOTO, JP, PAGES 941-944.		

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